

JC3-883

09/879841  
06/12/01

	Subclass	ISSUE CLASSIFICATION
	Class	

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>44m<sup>2</sup></i> Q.A. <i>LA</i>	PATENT DATE
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APPLICATION NO. 09/879841	CONT/PRIOR	CLASS <i>2</i>	SUBCLASS <i>2</i>	ART UNIT <i>21157</i>	EXAMINER <i>DO</i>
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APPLICANTS  
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TITLE  
Method and apparatus for optimizing the timing of integrated circuits

PTO-2040  
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	
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